Attorney Docket No. 0756-2131

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| n re Patent Application of: |) | Group Art Unit: 2811 |
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| Shunpei YAMAZAKI et al. |) | Examiner: S. Crane |
| Serial No. 09/535,015 |) | CERTIFICATE OF MAILING I hereby certify that this correspondence is |
| Filed: March 24, 2000 |) | being deposited with the United States Posta Service with sufficient postage as First Class |
| For: METHOD OF MANUFACTURING A |) | Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450 |
| SEMICONDUCTOR DEVICE |) | Alexandria, VA 22313-1450, or 12-23.04 |
| | | adele M Stamper |

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing information known to Applicant and requests that this information be made of record in the above identified application. Copies are submitted herewith in accordance with 37 C.F.R. 1.98(a).

A check in the amount of \$180 is being submitted to comply with the provisions of 37 C.F.R. § 1.97(c).

Respectfully submitted,

Eric J. Robinson

Reg. No. 38,285

Robinson Intellectual Property Law Office, P.C.

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Complete if Known Substitute for form 1449A/PTO 09/535,015 **Application Number** INFORMATION DISCLOSURE March 24, 2000 Filing Date STATEMENT BY APPLICANT Shunpei YAMAZAKI et al. First Named Inventor (use as many sheets as necessary) 2811 Group Art Unit S. Crane **Examiner Name** Sheet of 1 Attorney Docket Number 0756-2131 1

| | · | | | U.S. PATENT DOCUMEN | TS | |
|----------------------|--------------|----------------------|--------------------------------------|----------------------------------|--|---|
| Examiner Initials | Cite No.1 | U.S. Patent Document | | Name of Patentee or Applicant of | Date of Publication of Cited Document MM-DD-YYYY | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
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This will acknowledge receipt of the following:

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in re PATENT application of:

Shunpei YAMAZAKI et al.

Serial No. 09/535,015

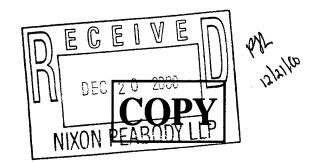
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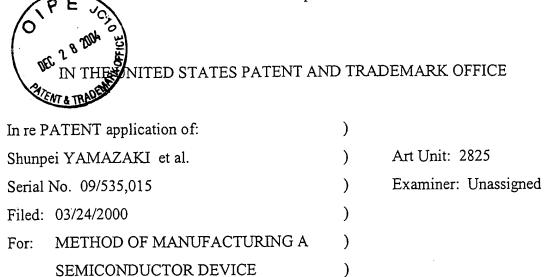
Title: METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE

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INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference(s) listed on the attached Form PTO-1449 be made of record in the above-identified application. Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The following two articles were cited by the Examiner in the following Applications:

Kuo, "Thin Film Transistor Technologies", Vol. 94-35, PP. 116-122, The Electrochemical Society Proceedings, cited in Serial No's 08/784,292 and 09/233,450.

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The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,

Eric J. Robinson Reg. No. 38,285

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Form PTO-1449 (Rev. 8-83) INFORMATION DISCLOSURE TO TENT

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Atty Docket 0756-2131

Serial No. 09/535,015

Applicants: Shupei YAMAZAKI et al.

Filing Date: March 24, 2000

Group Art Unit: 2825

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Form PTO-1449 U.S.D., artment of Commerce Serial No. 09/535.015 Atty Docket 0756-2131 Patent and Trademark Office (Rev. 8-83) Applicants: Shupei YAMAZAKI et al. INFORMATION DISCLOSURE STATEMENT Filing Date: March 24, 2000 Group Art Unit: 2825 **U.S. PATENT DOCUMENTS** Name Class Subclass Filing Date **Document Number** Date Examiner (if appropriate) Initial Moslehi 02/18/1992 5,089,441 05/12/1992 Mitra et al. 5,112,764 Sameshima et al. 09/08/1992 5,145,808 Liu et al. 09/15/1992 5,147,826 12/22/1992 Asakawa et al. 5,173,446 04/06/1993 Nakamura et al. 5,200,630 06/22/1993 Sugino et al. 5,221,423 07/06/1993 Sugino et al. 5,225,355 09/14/1993 Lim 5,244,836 10/19/1993 Tran 5,254,480 Yamazaki et al. 5,262,350 11/16/1993 Yamazaki 11/16/1993 5,262,654 Fonash et al. 01/04/1994 5,275,851 01/11/1994 Yonehara 5,278,093 02/22/1994 Yamazaki et al. 5,289,030 03/22/1994 Yamazaki et al. 5,296,405 Lagendijk et al. 03/29/1994 5,298,075 05/03/1994 Yamazaki et al. 5,308,998 05/17/1994 Zhang et al. 5,313,075 5,352,291 10/04/1994 Zhang et al. Oostra et al. 10/11/1994 5,354,697 10/25/1994 Wong 5,358,907 11/22/1994 Mei et al. 5,366,926 02/07/1995 Doyle et al. 5,387,530 Wakai 06/13/1995 5,424,230 Hamada 5,470,763 11/28/1995 Chiang et al. 01/02/1996 5,480,811 Miyasaka et al. 04/02/1996 5,504,019 04/16/1996 Horai et al. 5,508,207 06/25/1996 Yonehara et al. 5,530,266 07/02/1996 Yonehara · 5,531,182 Date Considered Examiner *EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if

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Form PTO-1449 U.S.L .rtment of Commerce Serial No. 09/535,015 Atty Docket 0756-2131 Patent and Trademark Office (Rev. 8-83) Applicants: Shupei YAMAZAKI et al. INFORMATION DISCLOSURE STATEMENT Group Art Unit: 2825 Filing Date: March 24, 2000 U.S. PATENT DOCUMENTS Class Subclass Filing Date Date Name Document Number Examiner (if appropriate) Initial 08/27/1996 Funai et al. 5,550,070 12/03/1996 Hsu et al. 5,580,815 04/08/1997 Makita et al. 5,619,044 08/26/1997 Takemura et al. 5,661,311 11/04/1997 Hwang 5,684,317 Tang et al. 11/04/1997 5,684,365 12/02/1997 Yamazaki et al. 5,693,541 12/02/1997 Inoue et al. 5,693,959 12/09/1997 Makita et al. 5,696,003 02/10/1998 Zhang 5,717,224 03/31/1998 Chang et al. 5,734,179 Takayama et al. 5,786,796 07/28/1998 08/18/1998 Kousai et al. 5,795,795 Mitanaga et al. 09/15/1998 5,808,321 Arai et al. 10/13/1998 5,821,560 Takemura 10/27/1998 5,828,429 11/17/1998 Sugawara 5,838,508 12/01/1998 Takayama et al. 5.843,225 12/08/1998 Ju 5,846,857 03/16/1999 Zhang et al. 5,882,960 Yamazaki et al. 03/30/1999 5,888,858 04/20/1999 Zhang et al. 5,895,933 05/04/1999 Yamazaki et al. 5,899,547 Yamazaki et al. 07/27/1999 5,929,464 08/03/1999 Yamazaki et al. 5,933,205 5,940,732 08/17/1999 Zhang Yamazaki et al. 5.962,869 10/05/1999 Yamazaki et al. 10/05/1999 5,963,278 Yamazaki et al. 11/16/1999 5,985,740 11/23/1999 Zhang 5,990,491 11/23/1999 Yamazaki 5,990,542 Date Considered Examiner *EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if

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Form PTO-1449 U.S.L artment of Commerce Atty Docket 0756-2131 Serial No. 09/535,015 (Rev. 8-83) Patent and Trademark Office Applicants: Shupei YAMAZAKI et al. INFORMATION DISCLOSURE STATEMENT Filing Date: March 24, 2000 Group Art Unit: 2825 FOREIGN PATENT DOCUMENTS Class Subclass Translation Country Date Examiner Document Number Yes Initial Abst. 05/21/1996 Japan 08-129360 Abst. 09/13/1996 Japan 08-234683 Abst. 09/17/1996 Japan 08-241047 Abst. 09/17/1996 Japan 08-241048 Abst. 09/17/1996 08-241057 Japan Abst. 09/17/1996 08-241997 Japan Full 06/10/1985 Japan 60-105216 Full 04/01/1986 61-063017 Japan Abst. 04/01/1986 Japan 61-063107 Abst. 07/25/1987 Japan 62-169356 Abst. 03/27/1989 Japan 64-081324 OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) Examiner Initial Caune et al., "Combined CW Laser and Furnace Annealing of Amorphous Si and Ge in Contact With Some Metals", January 1, 1989, pp. 597-604, Applied Surface Schience, Vol. 36 Stoemnos et al., "Crystallization of Amorphous Silicon By Reconstructive Transformation Utilizing Gold", March 18, 1991, pp. 1196-1198, Appl. Phys. Lett. 58(11) Kakkad et al., "Crystallized Si-Films By Low-Temperature Rapid Thermal Annealing of Amorphous Silicon", March 1, 1989, pp. 2069-2072, J. Appl. Phys. 65(5) Oki et al., "Effect of Deposited Metals On the Crystallization Temperature of Amorphous Germanium Film", 1969, pp. 1056, Jpn. J. Appl. Phys. 8 Kuznetsov et al., "Enhanced Solid Phase Epitaxial Recrystallization of Amorphous Silicon Due to Nickel Silicide Precipitation Resulting From Ion Implantation and Annealing", 1993, pp. 990-993, Nucl. Instruments Methods Physics Research, 880/881 Takenaka et al., "High Mobility Poly-Si Thin Film Transistors Using Solid Phase Crystallized a-Si Films Deposited by Plasma Enhanced Chemical Vapor Deposition", December 1990, pp. L2380-L2383, Jpn. J. Appl. Phys. Vol. 29, No. 12 Hatalis et al., "High Performance Thin-Film Transistors in Low Temperature Crystallized LPCVD Amorphous Silicon Films", August 1987, pp. 361-364, Elec. Dev. Letters Vol. EDL 8, No. 8 Zorabedian et al., "Lateral Seeding of Silicon-on-Insulator Using an Ellipitical Laser Beam: A Comparison of Scanning Methods", 1984, pp. 81-86, Mat. Res. Soc. Symp. Proc. Vol. 33 Spaepen et al., "Metal-Enhanced Growth of Silicon", 1992, pp. 483-499, Crucial Issues in Semiconductor Materials & Processing Technologies Green et al., "Method To Purify Semiconductor Wafers", October 1973, pp. 1612-1613, IBM Tech. Discl. Bulletin Vol. 16, No. 5 Date Considered Examiner *EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this formwith next communication to applicant.

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| Form PTO-1 (Rev. 8-83) | U.S.Ltment of Commerce Patent and Trademark Office | | Serial No. 09/535,015 | | |
| INFORMATION DISCLOSURE STATEMENT | | Applicants: Shupei YAMAZAKI et al. | | | |
| | | Filing Date: March 24, 2000 | Group Art Unit: 2825 | | |
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